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		1001 001 IDE	Application Number			
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STATI	EMENT RY	APPLICANT	First Named Inventor	Alina Deutsch		
יואוי		ALL EIGHT	Group Art Unit			
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χς		RUI WANG, OMAR WING; Transient Analysis of Dispersive VLSI Interconnects Terminated in Nonlinear Loads; IEEE Transactions on Computer-Aided Design, Vol. 11 No. 10 October 1992; pages 1258-1277				
ALBERT E. RUEHLL AND THOMAS A. JOHNSON; Circuit Analysis Computing of Semiconduc						
		Packages and Systems; Wiley Encyclopedia of Electrical and Electronies Engineering, Vol. 3; pages 339-354				
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